

UTILITY PATENT APPLICATION TRANSMITTAL

Attorney Docket No. CBL-104/DIV

Total Pages

First Named Inventor

GLENN S. SOLOMON ET AL.

Title

DUAL PROCESS SEMICONDUCTOR HETEROSTRUCTURES & METHODS

APPLICATION ELEMENTS	ACCOMPANYING APPLICATION PARTS		
1. [X] Fee Transmittal Form	9. [] Assignment cover sheet and document(s)		
2. [] Applicant Claims Small Entity Status	10. [] Power of Attorney by Assignee		
3. [X] Specification Total Pages: [21]	[] with CFR 3.73(b) statement		
4. [X] Drawing(s) Total Sheets: [5]	11.[] English Translation Document		
5. [X] Oath or Declaration Total Pages: [I]	12. [X] IDS/PTO-1449		
a. [] Newly executed (original or copy)	[] with copies of cited references		
b. [X] Copy from a prior application 1.63(d)	13. [X] Preliminary Amendment		
(complete Box 17 and note Box 5 below)	14. [X] Return Receipt Postcard (MPEP 503)		
i. [] Signed statement deleting inventors named in the prior application, see CFR	15. [] Small Entity Statement		
1.63(d)(2) and 1.33(b)	[] Statement filed in prior application		
6. [X] Incorporation by Reference (if 5b is checked) The entire disclosure of the prior application, from which a	Status still proper and desired		
copy of the oath or declaration is supplied under Box	16. [] Certified Copy of Priority Document(s)		
5b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated	(if foreign priority is claimed)		
therein by reference.	17. [] Request and Certification under 35 USC 122(b)(2)(B)(i). Applicant must attach form		
7. [] Microfiche Computer Program (Appendix)	PTO/SB/35 or its equivalent		
8. [] Nucleotide/Amino Acid Sequence Submission (all the following are necessary)			
a. [] Computer Readable Copy	18. [X] Other: Copies of Assignment documents and Powers of Attorney by Assignees from parent application 09/293,620.		
b. [] Paper Copy (identical to computer copy)			
c. [] Statement verifying identity of above copies			
19. If a CONTINUING APPLICATION, check appropriate box	and supply the requisite information:		
[] continuation [X] divisional [] CIP	of prior application No.: [09/293,620]		

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FEE TRANSMITTAL

Application Number:	Not yet assigned
Filing Date:	Not yet assigned
First Named Inventor:	Glenn S. Solomon et al.
Title of Invention:	DUAL PROCESS SEMICONDUCTOR HETEROSTRUCTURES &
	METHODS
Group Art Unit:	Not assigned
Examiner:	Not assigned
Agent's Docket No.:	CBL-104/DIV

Fee Calculation: for [X] Large Entity / [] Small Entity.		
Basic Billing Fee: [X] Utility Patent Application: \$740/\$370		\$ 740
Provisional Patent Application: \$150 / \$75		\$
Claims:		
[] Number of Total Claims Over 20: []	x \$18/\$9 =	\$
No. of Independent Claims Over 3: []	x \$80 / \$40 =	\$
Other Fees:		
[] Extension of time, 1 month	\$110 / \$55	\$
[] Extension of time, 2 months	\$400 / \$200	\$
[] Extension of time, 3 months	\$920 / \$460	\$
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[] Missing Parts Surcharge (Regular Application)	\$130 / \$65	\$
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